

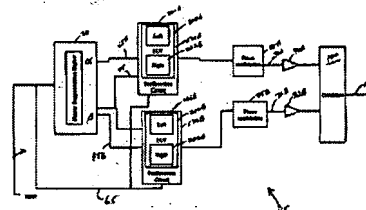
☒ Active

- Failed**

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- S1: (15) chireix near3 architecture
- S2: (9) S1 and predistortion
- S3: (9) S2 and (component adj signal)
- S4: (9) S3 and RF

DATE: 10/10/2014

(31) Int. Cl.⁷ H04M 4/00

	U	1	Document ID	Issue Date	Pages	Title	Current OR	Current	Ref	Inventor
1	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 20050003770 A1	20050106	14	Predistortion circuit for a transmit system	455/114.3	455/114.2		Saed, Aryan
2	<input type="checkbox"/>	<input type="checkbox"/>	US 20050001678 A1	20050106	25	Adaptive predistortion for a transmit system with gain, pha	330/149			Saed, Aryan
3	<input type="checkbox"/>	<input type="checkbox"/>	US 20050001677 A1	20050106	23	Adaptive predistortion for a transmit system	330/149			Saed, Aryan

☒ Plurals

Default operator:
☒ Highlight all hit terms initially

455/114.3 455/115.1 455/120-126 455/291-293 330/84
 330/107 330/136 330/149 330/280 330/124R